

438	424
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO. 09/621717	CONT/PRIOR	CLASS 438	SUBCLASS 424	ART UNIT 2842 2822	EXAMINER Thomas
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APPLICANTS

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Kaichiu Wong

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TITLE

Structure and method for monitoring a semiconductor process, and method of making such a structure .

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
4	38	424		438	149	423	692					
INTERNATIONAL CLASSIFICATION												
H	O	L	31/76									

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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 9	Figs. Drwg. 20	Print Fig. 1C	Total Claims 17	Print Claim for O.G. 1
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<u>Josie M. Thomas</u> <u>05/29/63</u> (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	(Primary Examiner) (Date) _____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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